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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

JCH/ase

APPL NUM 10058319	FILING DATE 01/30/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER L. Thon
<b>**APPLICANTS:</b> Miyazaki Chuichi; Akiyama Yukiharu; Shibamoto Masanori; Kudaishi Tomoaki; Anjoh Ichiro; Nishi Kunihiro; Nishimura Asao; Tanaka Hideki; Kimoto Ryosuke; Tsubosaki Kunihiro; Hasebe Aki;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CON OF 09/768,451 01/25/2001 WHICH IS A CON OF 09/449,834 11/26/1999 PAT 6,342,726 WHICH IS A CON OF 08/822,933 03/21/1997 ABN  L. Thon					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 8-66637 03/22/1996  L.T.					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials L.T.				ATTORNEY DOCKET NO 501.35286C14	
TITLE : Semiconductor device and manufacturing method thereof <small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-436 (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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